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				Application Number	09/881628
				Filing Date	06/13/01
				First Named Inventor	Enric Musoll
				Art Unit	2661
Sheet	2	of	4	Examiner Name	
				Attorney Docket Number	MIPS.0174-00-US

FOREIGN PATENT DOCUMENTS						
Examiner Initials *	Cite No. *	Foreign Patent Document Country Code* Number* Kind Code* (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T*
Km	AA	WO 03/005645A2	1/2003 -06-25-2002	Sampath et al.		Yes

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. *	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T*
Km	BA	MELVIN ET AL., "Extended Instruction Set for a Packet Processing Applications," July 5, 2001, Disclosure Document #496559, USPTO	
Km	BB	MUSOLL ET AL., "Hardware Algorithm for Allocating and De-Allocating Consecutive Blocks of Memory," April 3, 2001, Disclosure Document #491557, USPTO	
Km	BC	MUSOLL ET AL., "Mechanism to Overflow Packets to a Software Controlled Memory When They Do Not Fit into a Hardware Controlled Memory," July 3, 2001, Disclosure Document #496391, USPTO	
Km	BD	MUSOLL ET AL., "Mechanism for Allowing a Limited Packet Head and/or Tail Growth Without Moving the Packet to a Different Memory Location," April 16, 2001, Disclosure Document #492429, USPTO	
Km	BE	MUSOLL ET AL., "Mechanism to Activate a Context When No Stream is Running in a Multi-Streaming Processing Core," April 16, 2001, Disclosure Document #492431, USPTO	
Km	BF	MUSOLL, ENRIC, "Functional Validation of a Packet Management Unit," May 18, 2001, Disclosure Document #429011, USPTO	
Km	BG	SAMPATH ET AL., "Mechanism to Un-speculatively Pre-fetch Instructions from the Thread Associated to a Packet," April 2, 2001, Disclosure Document #491527, USPTO	
Km	BH	YAMAMOTO, WAYNE. An Analysis of Multistreamed, Superscalar Processor Architectures. University of California Santa Barbara Dissertation. December 1995. Santa Barbara, US.	
Km	BI	YAMAMOTO ET AL. "Increasing Superscalar Performance Through Multistreaming." Parallel Architectures and Compilation Techniques (PACT '95). 1995	
Km	BJ	The PowerPC Architecture: A Specification for a New Family of RISC Processors. 2 nd Ed. May 1994. pp. 70-72. Morgan Kaufmann. San Francisco, US.	

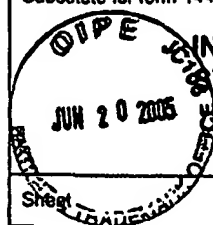
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Sheet	1	of	4
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Examiner Initials*	Cite No.†	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
		Number-Kind Code* (if known)			
KM		4200927	04-29-1980	Huges et al.	
KM		5708814	1-13-1998	Short et al.	
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KM		5559970	09-24-1996	Vinod Sharma	
KM		6614796	09-02-2003	Black et al.	

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